

# IncA: A DSL for the Definition of Incremental Program Analyses

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## ABSTRACT

Program analyses support software developers, for example, through error detection, code-quality assurance, and by enabling compiler optimizations and refactorings. To provide real-time feedback to developers within IDEs, an analysis must run efficiently even if the analyzed code base is large.

To achieve this goal, we present a domain-specific language called IncA for the definition of *efficient incremental program analyses* that update their result as the program changes. IncA compiles analyses into graph patterns and relies on existing incremental matching algorithms. To scale IncA analyses to large programs, we describe optimizations that reduce caching and prune change propagation. Using IncA, we have developed incremental control flow and points-to analysis for C, well-formedness checks for DSLs, and 10 FindBugs checks for Java. Our evaluation demonstrates significant speedups for all analyses compared to their non-incremental counterparts.

## CCS Concepts

•Software and its engineering → Automated static analysis; Data flow languages; Integrated and visual development environments;

## Keywords

Static Analysis; Incremental Computation; Domain-specific Language; Language Workbench

## 1. INTRODUCTION

Static program analysis is the basis of compiler optimizations and IDE features such as error detection or behavior-preserving refactorings. Program analyses trade off precision for runtime performance and memory use, and there is a large body of research on techniques that enable increasingly precise and efficient analyses. For example, by giving up flow-sensitivity and context-sensitivity, points-to analysis can analyze millions of lines of Java code in under a minute [9].

Our work improves the performance of program analyses through incrementality: When part of the code changes (for

example, through user edits), we only reanalyze the changed part, plus all the code whose analysis result depends on the changed results. This way, incremental program analysis can provide significant improvements compared to reanalyzing the whole code base from scratch. Such incremental analyses are useful in IDEs that perform real-time analysis upon user edits and in continuous integration servers that continuously analyze an evolving code base.

We present IncA, a domain-specific language (DSL) for the definition of efficient incremental program analyses, as well as an optimizing compiler and a runtime system for IncA. Conceptually, IncA represents computations as *graph patterns* [30] on top of the abstract syntax tree (AST) of the analyzed program. Graph patterns express relationships between AST nodes, for example, to describe the control flow between individual statements of the analyzed program. The IncA compiler translates a user-defined program analysis into a set of interconnected graph patterns. The IncA runtime system maintains the analysis results incrementally by performing incremental graph pattern matching.

Semantically, a graph pattern describes a set of tuples that relate program entities. However, using graph patterns is difficult for many developers, because, instead of mapping input to output, they construct tuples for related entities by splitting, joining and filtering sets of tuples. IncA introduces *pattern functions* to abstract from graph patterns: Instead of operating on sets, a pattern function takes a single input and either rejects it or computes a corresponding output. This way, a pattern function defines what we call the primary linearization of the underlying graph pattern, for example, in the style of a forward or backward analysis. Our compiler translates pattern functions into regular graph patterns.

We have implemented IncA on top of JetBrains MPS,<sup>1</sup> an IDE that relies on projectional editing, where code changes occur in the form of user-issued AST change requests. This aligns well with incrementalization because each AST change triggers an incremental update of the analysis results. The design and implementation of IncA is independent of the analyzed language and we developed a generic architecture for the integration of IncA into other IDEs.

To evaluate IncA, we implemented incremental analyses for C and Java programs and measured their runtime performance. For C, we developed incremental control flow analysis, flow-sensitive points-to analysis, and domain-specific well-formedness checks. For Java, we reimplemented 10 FindBugs analyses [20]. Our evaluation shows that IncA-based incremental program analyses yield significant speedups without

<sup>1</sup><https://www.jetbrains.com/mps>

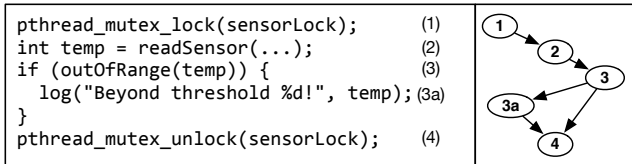


Figure 1: A simple C program and its control flow

introducing unacceptable memory or initialization overheads. In summary, we make the following contributions:

- We introduce IncA, a language for the definition of incremental program analyses that is independent of the analyzed language. Our compiler translates IncA code into graph patterns (Section 3).
- We implement compiler optimizations for IncA that reduce the memory required for incrementalization and the time required for change propagation (Section 4).
- We describe the runtime system of IncA as a generic architecture that allows the integration of IncA analyses into different IDEs. Technically, the runtime system employs incremental graph pattern matching on the AST of the analyzed program (Section 5).
- We develop three incremental analyses for C, including an incremental flow-sensitive points-to analysis, and reimplement 10 FindBugs analyses for Java (Section 6).
- We evaluate the memory and runtime performance of IncA through extensive case studies on real-world C and Java software projects (Section 7). We show that program analyses developed with IncA provide real-time feedback and scale to large code bases.

We reuse the IncQuery [35] incremental evaluator in our system; thus the core incremental evaluator is not our contribution. There exist several incremental algorithms for a particular analysis [7, 25, 18, 26] and frameworks for a particular class of analyses [3, 14, 39] in the literature. In contrast to these solutions, our analysis framework is not bound to a specific class of analyses (Section 6) and employs incremental graph pattern matching for program analysis as a novel approach.

## 2. INCREMENTAL PROGRAM ANALYSIS BY EXAMPLE

Using control flow analysis for C as an example, this section explains the problem of incremental program analysis and illustrates our solution.

**Control flow analysis** IDEs and compilers use control flow analysis to reason about the execution order of statements in a program and as a building block for further analyses such as points-to analysis.

The input of control flow analysis is the AST of the program and its output is a control flow graph (CFG) [24]. As an example, consider the C program and its control flow in Figure 1. Each node in the CFG represents a statement in the program. A source statement is connected to a target statement in the CFG if there exists an execution trace in which the execution of the source statement immediately precedes the execution of the target statement. Note how control can flow from statement 3 both to 3a and 4 in Figure 1, depending on the condition of the if statement, which is why the CFG contains edges from 3 to 3a and from 3 to 4.

The result of our control flow analysis is a set of CFG edges. In this section, we restrict ourselves to a subset of

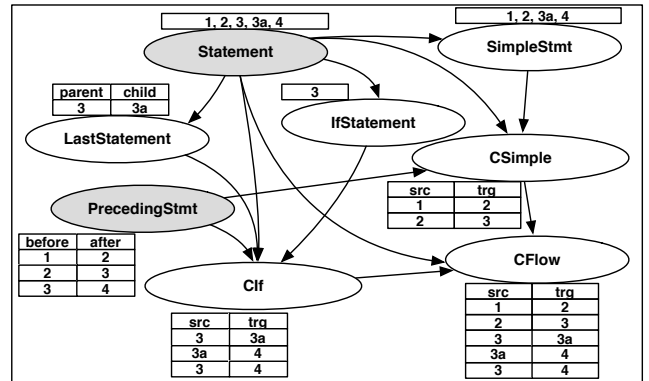


Figure 2: Computation graph of the CFG analysis evaluation

C with if statements and simple statements that entail a sequential control flow such as assignments. We define the relation **CFlow** for the edges of the CFG and we compute it as the union of two helper relations. (1) **CSimple** consists of those control flow edges (**src**, **trg**) where **src** is a simple statement that syntactically precedes **trg** in the source code. (2) **CIf** consists of control flow edges (**src**, **trg**) that lead into or out of an if statement or its branches, which is the case if (a) **src** syntactically precedes **trg** and **src** is an if statement without an **else** part, (b) **src** is an if statement and **trg** is the first statement of one of its branches, or (c) **src** is the last statement of one of the branches of an if that syntactically precedes **trg**.

In our example, **CSimple** has two elements (1,2) and (2,3), while **CIf** has three elements (3,3a), (3a,4), and (3,4). The union of the two relations constitutes **CFlow**.

**Evaluation with graph patterns** To compute the elements of the relations **CSimple**, **CIf**, and **CFlow**, we need to traverse the AST and discover the relevant structural relations between the AST nodes and their attributes. A natural representation of such computations is a *graph pattern* [30]. Much like our informal description of the relations above, a graph pattern describes sets of related entities through structural constraints on AST nodes and on instances of other graph patterns.

Given a set of interconnected graph patterns, we can compute their results by using a *computation graph* as illustrated in Figure 2 for our example. A computation graph consists of two kinds of nodes, each of which yields a set of related entities. *Input nodes* (grey) represent the AST structure directly, do not perform any computation, and do not depend on any other node. In Figure 2, **Statement** and **PrecedingStatement** are the only input nodes; the former enumerates the nodes of type **Statement** from the AST and the latter enumerates the pairs of statements where the first statement syntactically precedes the other. *Computation nodes* (white) use the results of input nodes and other computation nodes to relate program entities. For example, node **CSimple** uses information from node **Statement**, **SimpleStatement**, and **PrecedingStatement** to identify statements related through simple control flow. Node **CFlow** combines the results from two computation nodes to produce the complete CFG.

**Incremental control flow analysis** We encode program analyses as graph patterns and computation graphs because this provides a good basis for incrementalizing the computation. Suppose the user modifies the analyzed program and adds an **else** branch 3b to the if statement as illustrated in

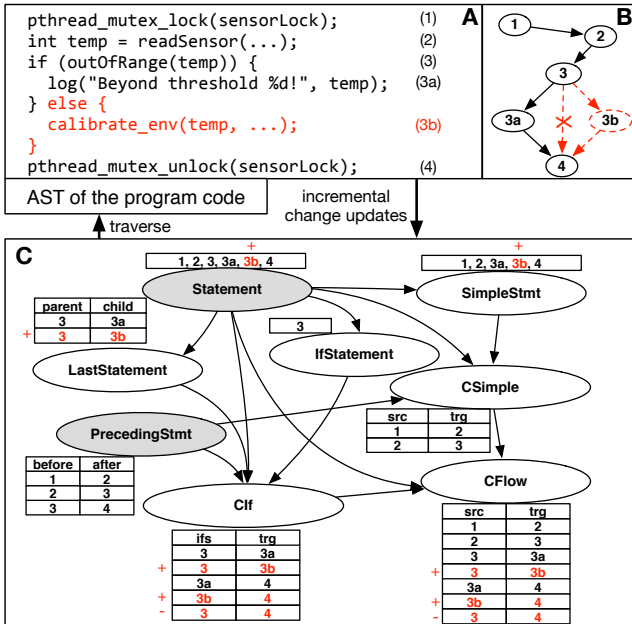


Figure 3: Example analysis evaluation; (A) example C program code after modification, (B) CFG of the program code, (C) computation graph for incremental evaluation

Figure 3. This invalidates the old CFG. Using graph patterns and computation graphs, instead of recomputing a new CFG from scratch, we can *incrementally update* the existing CFG.

To this end, computation graphs use memoization and perform incremental graph pattern matching: When code gets changed, we send change events to the input nodes of the computation graph. The nodes then transitively propagate changes to all dependent computation nodes and trigger the reanalysis of changed program entities. This way, we avoid any reanalysis of unchanged parts of the program. In our example, the introduction of the `else` branch triggers the following changes in the computation graph (cf. Figure 3):

- Addition of (3b) to `Statement` and `SimpleStatement`.
- Removal of (3,4) from `CIf` because `if` statement 3 now has an `else` branch. That is, the conditional treatment is exhaustive and control is guaranteed to pass through one of the branches before reaching 4.
- Addition of (3,3b) to `CIf` because 3b is the first statement of the `else` branch.
- Addition of (3,3b) to `LastStatement` because 3b is the last statement of `else` branch of `if` statement 3.
- Addition of (3b,4) to `CIf` because (3,3b) was added to `LastStatement` and 3 precedes 4.
- Propagation of all changes from `CIf` to `CFlow`.

In our implementation based on projectional editing, we receive change events for user-issued AST changes directly from the IDE. Fine-grained AST change notifications align perfectly with incrementalization and there is no need for a potentially costly parsing step to compute AST differences. After an AST change, a projectional editor derives a new projection from the AST and displays it to the programmer.

**A DSL for program analysis** A DSL based on graph patterns is a good semantic basis for incrementally analyzing programs. However, programming with graph patterns is difficult because graph patterns operate on sets of related program entities, using operations such as filter, map, and

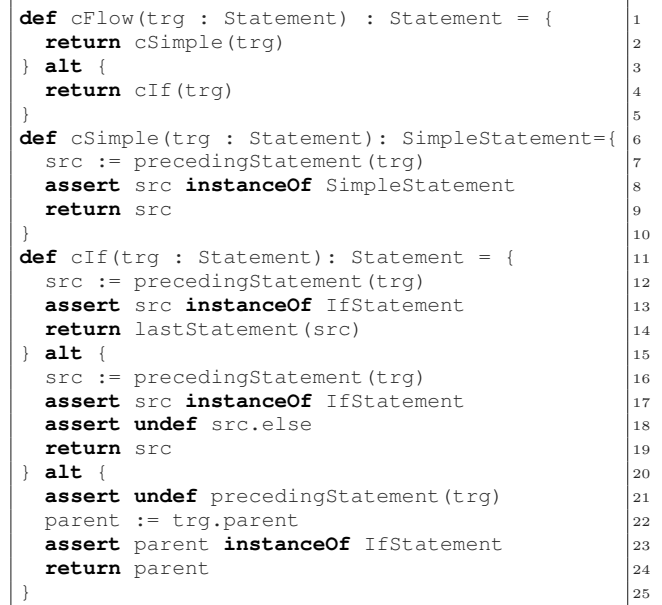


Figure 4: Control flow analysis for a subset of C in InCA

cross product. This is far removed from the usual way of defining program analyses as forward or backward analyses [24]. Additionally, considerable anecdotal evidence at companies *itemis* and *IncQueryLabs*, where developers use the IncQuery graph pattern language [35] for commercial projects, shows that developers would rather rely on more familiar core abstractions; functions, distinguishing input from output, direction in the function body and assignments. This experience was the main driver when we designed a DSL called IncA which abstracts from graph patterns.

Instead of operating on sets of program entities, IncA supports the definition of program analyses using what we call *pattern functions*. A pattern function is a linearized graph pattern: It takes a single tuple of inputs and either rejects the input or produces a single tuple of outputs through a sequence of statements in the body of the pattern function. Semantically, a pattern function corresponds to a graph pattern that relates the program entities that occur as either input or output. In particular, the separation of entities into input and output and the order of statements in the body of a pattern function are semantically irrelevant. Nevertheless, linearization simplifies the definition of program analyses. Since many different linearizations of the same graph pattern are possible, we say a pattern function defines the *primary linearization* of the underlying graph pattern. We illustrate IncA through the definition of the control flow analysis for our subset of C, where the linearization enables us to write the definition in the style of a backward analysis.

Figure 4 shows the IncA code that defines the control flow analysis. We show three pattern functions that follow the informal description of the corresponding relations above, while the other helper functions are omitted. Function `cFlow` takes a target statement `trg` and finds and returns the control flow predecessors of `trg` using functions `cSimple` and `cIf`. We write `alt` to provide alternative results for a pattern function. Note that the functions are defined over the mbeddr C [37] dialect in this example. The tight connection with the analyzed language makes it possible to use its types and language concepts in the IncA code and to provide the usual

IDE features, such as highlighting and proposals, during the development of the analysis.

Function `cSimple` queries pattern `precedingStatement` for `trg`. The function can return multiple results, but `IncA` abstracts over this. Next, `cSimple` asserts that the predecessor of `trg` is indeed a simple statement. If it is, `cSimple` yields this statement as a control flow predecessor. Otherwise, `cSimple` fails for `trg` and does not yield any output.

Function `cIf` is composed of three alternatives that compute the control flow when an `if` statement is involved. `IncA` provides direct access to the AST structure. For example, in the second alternative, `src.else` represents the `else` branch of the `if` statement `src`. As shown by `cIf`, `IncA` also supports the language construct `undef` to ascertain that a given pattern-function call does not yield any result or that an AST node is undefined.

Our compiler translates pattern functions into regular graph patterns, but also analyzes the pattern functions to perform optimizations. In particular, our compiler determines which parts of an AST are irrelevant for an analysis and uses this information to prune the propagation of change notifications and to reduce caching (Section 4).

### 3. INCREMENTAL PROGRAM ANALYSIS WITH `IncA`

In this section we discuss `IncA` in greater detail. Specifically, we describe the syntax of `IncA` and explain how we translate its syntactic constructs into graph patterns.

#### 3.1 Syntax of `IncA`

Figure 5 shows the syntax of `IncA`. We write  $\bar{a}$  for a sequence of  $a$  elements, which includes the empty list.

A *module* groups related pattern functions. Modules can import other modules to gain access to their public pattern functions. Functions are public by default, but can be marked private to restrict their visibility to the containing module.

A *pattern function* represents the primary linearization of a graph pattern. A function takes a tuple of typed input parameters and either rejects the input or produces an output tuple. A function may define multiple *alternative* linearizations for the same input.

Each alternative consists of a sequence of *statements*, ending with a return statement that defines the output tuple. An assignment statement binds variables to the components of a tuple. An assertion statement aborts the computation and rejects the current input if the condition fails. As *conditions* we support equality, inequality, AST node-type membership, AST node-type exclusion, and undefinedness testing.

An *expression* can refer to a variable, represent a value literal, refer to a property of an AST node, call another pattern function, or compute the transitive closure of another pattern function. For the transitive closure, the called pattern function must take a single input and provide a single output. The evaluation of such expressions yields all intermediate outputs, but `IncA`'s syntax abstracts over this and permits the use as if there was only a single definite output.

#### 3.2 Compilation to Graph Patterns

The theory of graph patterns is well established [30] and we define the semantics of `IncA` through translation to graph patterns. A graph pattern consists of pattern variables and constraints over these variables. The constraints can refer to other graph patterns. We use the following constraints:

(module)	$m ::= \text{module } n \text{ import } \bar{n} \{ \bar{f} \}$
(function)	$f ::= vis \text{ def } n(\bar{n} : \bar{T}) : \bar{T} = \bar{a}$
(visibility)	$vis ::= \text{private} \mid \text{public}$
(alternative)	$a ::= \bar{s}$
(statement)	$s ::= \bar{n} := e \mid \text{assert } c \mid \text{return } e$
(condition)	$c ::= e == e \mid e != e \mid e \text{ instanceof } T \mid e \text{ not instanceof } T \mid \text{undef } e$
(expression)	$e ::= n \mid l \mid e.n \mid n(\bar{e}) \mid n^+(e)$
(literal)	$l ::= \text{number} \mid \text{string} \mid \text{enum} \mid \text{boolean}$
(type)	$T ::= \text{AST node type (from analyzed language)}$
(name)	$n ::= \text{name}$

Figure 5: Syntax of `IncA`

- **Entity**( $v, T$ ) holds if variable  $v$  has type  $T$ .
- **Relation**( $l, v1, v2$ ) holds if there is an edge labeled  $l$  between variables  $v1$  and  $v2$ .
- **Eq**( $v1, v2$ ) and **Neq**( $v1, v2$ ) hold if variables  $v1$  and  $v2$  point to the same/different element.
- **PC**( $p, \bar{v}$ ) and **NPC**( $p, \bar{v}$ ) hold if the pattern  $p$  accepts/rejects the tuple  $\bar{v}$ .
- **TC**( $p, v1, v2$ ) holds if the transitive closure of the binary pattern  $p$  contains the tuple  $(v1, v2)$ .
- **Alt**( $\bar{p}, \bar{v}$ ) holds if any of the patterns in  $\bar{p}$  accepts the tuple  $\bar{v}$ .

We map `IncA` constructs to graph-pattern constraints as follows. An `IncA` variable becomes a pattern variable and a literal becomes a pattern variable with an **Eq** constraint. A property access  $e.n$  translates to a constraint **Relation**( $n, e, v$ ), where  $v$  is a fresh pattern variable that represents the result of the lookup. A function call  $f(\bar{e})$  becomes a constraint **PC**( $f, \bar{e}, \bar{v}$ ), where  $\bar{v}$  are fresh and represent the result of the call (the notation  $\bar{e}\bar{v}$  means concatenation). For transitive closure, we use the **TC** constraint.

Next, we translate `IncA` conditions to constraints. Equality and inequality straightforwardly translate to **Eq** and **Neq** constraints, and node-type membership test with `instanceof` becomes an **Entity** constraint. Negative conditions require helper patterns and calls to them with **NPC**. The helper function contains an **Entity** for a `not instanceof` while it contains the corresponding subpattern for the expression in case of an `undef`. The function call with an `undef` is an exception, because it is directly translated to an **NPC**.

For statements, we proceed as follows. An assertion simply promotes the constraints of its condition. An assignment matches up the variables on the left-hand side with the variables that result from the expression on the right-hand side. We generate **Eq** constraints for the pairs of variables from the two sides. To represent the output tuple of a function, we use designated variables. We handle a return statement as an assignment to these variables.

Finally, we collect the functions from a module and its transitively imported modules and generate graph patterns for the functions and their alternatives. Function input parameters become pattern variables  $\bar{v}_i$  and we create designated pattern variables  $\bar{v}_o$  for the output tuple. The type annotations on input and output of a function become **Entity** constraints over  $\bar{v}_i \bar{v}_o$ . We combine the patterns  $\bar{p}$  of a function's alternatives using an alternative constraint **Alt**( $\bar{p}, \bar{v}_i \bar{v}_o$ ).

This translation process also shows the driving forces for the abstraction from graph patterns. Graph patterns are verbose, because they require explicit variables for *all* intermediate expressions, explicit **Entity** for type constraints and helper patterns for negative conditions. Additionally, they are nondirectional which is in contrast to the usual directional nature of program analyses.

To achieve incrementality, we reuse the incremental graph pattern-matching implementation that is part of IncQuery [35], but there are also many other alternatives for matching [30]. The expressive power of our program analysis language (and of the core IncQuery engine) is classified as FO(LFP) [28, 21] which stands for First Order logic extended with the Least Fixed Point operator. We believe that this formalism is expressive enough for a wide variety of program analysis as we demonstrate in Section 6.

## 4. COMPILER OPTIMIZATIONS FOR IncA

The performance of IncA depends on both the memory required for caching as well as the efficiency of change propagation in the computation graph. In this section, we describe compiler optimizations for IncA that improve both of them.

Our approach for performance improvement relies on the observation that the evaluation of a program analysis usually only depends on a relatively small part of an AST. For incremental analysis, this means that many AST changes do not affect the analysis result and can be safely ignored. We can use this observation to improve caching and change propagation. There is no need to write a changed element to the input nodes of the computation graph if it is known to be irrelevant for the analysis. This saves both memory and time, because by discarding irrelevant input nodes we avoid subsequent change propagation in the computation graph, which in turn also avoids caching of unneeded results.

To make use of this observation in practice, we must be able to distinguish relevant from irrelevant changes. To this end, we have developed an analysis for IncA programs, that is, an *analysis of the program analysis* code. Our analysis inspects the IncA code to compute a conservative approximation of all changes that can affect the result of the IncA program. We analyze each module of the IncA program with its transitive imports separately because the IncA compiler creates one computation graph for each module.

As a first approximation of the relevant changes, we can collect the *declared types* of all mentioned AST nodes from the functions. For example, consider again the control flow analysis in Figure 4. The IncA code refers to AST nodes of type `Statement`, `SimpleStatement`, `IfStatement`, and `ElsePart` (via `src.else`). Since type `Statement` is a supertype of the other two statement types, a sound approximation is given by the set of changes that modify nodes of type `Statement` or `ElsePart`. Accordingly, when incrementally executing the control flow analysis in the IncA runtime system, we can ignore changes to expressions, function declarations, and others. Note that the type information is not an additional assumption about the analysis, it is part of the IncA code. The subtyping relationship between the AST node types is determined by the analyzed program’s language.

Using the declared types of AST nodes is a good starting point for optimization; however it is rather imprecise. To improve the effectiveness of the optimization, we can take type assertions (`instanceOf`) into account to determine the actually relevant AST node types. Consider an excerpt of a points-to analysis for C shown in Figure 6. Function `pointsTo` computes pairs of C variables for assignments of the form `u = &v`. It uses function `varInExpr` to reject expressions that are not variables (such as additions or multiplications) and otherwise to extract the variable of an expression.

If we only consider the declared types and ignore type assertions, we have to assume that a change to any node

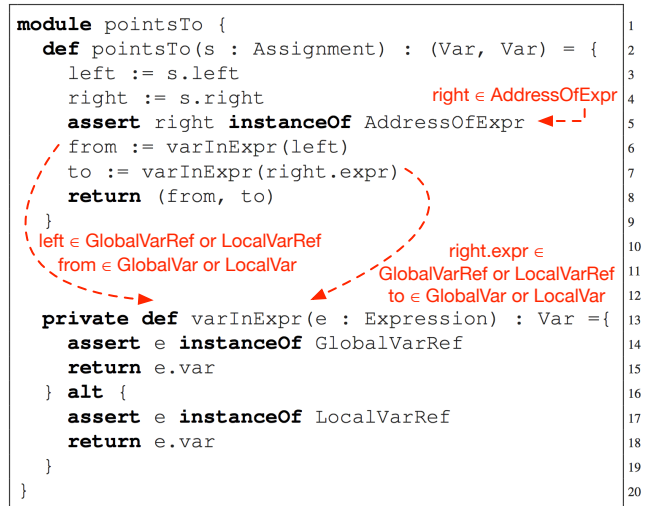


Figure 6: Identifying relevant AST node types in IncA

of type `Assignment`, `Var`, or `Expression` affects the analysis result. However, we can apply the following reasoning to narrow the set of relevant changes in Figure 6:

- Initially, both variables `left` and `right` have type `Expression` according to the properties of `Assignment`.
- The type assertion at line 5 constrains the type of `right` to the more specific type `AddressOfExpr`.
- The function call in line 6 calls `varInExpr` with a reference to the variable `left`. Function `varInExpr` has two alternatives, the first one restricting `e` to `GlobalVarRef`, while the second one restricts it to `LocalVarRef`. Expressions that satisfy neither restriction are rejected by `varInExpr`. So the call in line 6 only succeeds, and thus contribute to the analysis result, if `left` is a `GlobalVarRef` or a `LocalVarRef`. Moreover, the type of variable `from` must be `GlobalVar` or `LocalVar`.
- The same reasoning applies to the function call at line 7 for `right.expr` and variable `to`.

This reasoning shows that we only need to observe changes to statement nodes of type `Assignment`, to expression nodes of type `AddressOfExpr`, `GlobalVarRef`, and `LocalVarRef` and to variable nodes of type `GlobalVar` and `LocalVar`. Considering that, for example, the mbeddr dialect of C with its language extensions has more than 200 different kinds of expressions, our analysis can yield significant improvements for the memory and runtime performance of an IncA analysis. We empirically confirm this in Section 7.

We now provide a detailed description of our interprocedural data-flow analysis for IncA programs:

**Traverse call chains** We perform a depth-first traversal of the pattern-function call graph, starting at publicly visible functions. The analysis is inter-procedural, that is, we pass type information from the caller to the callee. Different call chains may result in different type constraints for parameters and variables; thus we analyze all call chains separately.

**Type intersection** During traversal, for each alternative of a pattern function, we collect the type constraints from assertions and function calls for each parameter and local variable. An alternative can only succeed if each parameter and variable satisfies all type constraints. This corresponds to constructing the intersection of all type constraints for each parameter and variable.

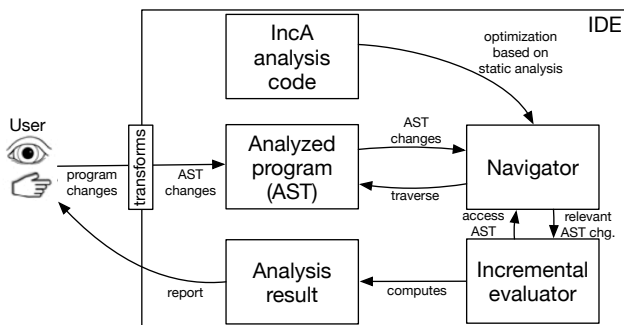


Figure 7: Architecture for integrating IncA into IDEs

**Type union** A pattern function succeeds if at least one of its alternatives succeeds. Thus, it is sufficient if the parameters satisfy the type constraints of at least one alternative. We approximate this by constructing the union of the type constraints for parameters and variables from all alternatives.

**Optimization** The analysis result is the union of the type constraints for the different call chains. Only changes that affect nodes of these types can affect the analysis result. We prune the propagation of irrelevant changes, thus avoiding the computation and caching of irrelevant pattern matches.

As opposed to using a library for program analysis, an advantage of specifying the program analysis as DSL code is that the analysis itself becomes analyzable which enables our optimization. Additionally, our optimization is generic as it is not bound to the IncA DSL, for example, *it could be applied on the graph patterns* as well.

## 5. TECHNICAL REALIZATION AND IDE INTEGRATION

We elaborate on the architecture of IncA’s runtime system, identifying components that enable integration of incremental program analyses into IDEs and explain their interactions.

### 5.1 Overview

Figure 7 shows the architecture for integrating IncA into an IDE. The analysis code is an IncA program that runs on the analyzed program. As detailed in Section 5.3, our architecture requires that the IDE translates user edits into AST change notifications, which trigger the incremental analysis.

The navigator is the entry point of the incremental analysis. As an adapter between the IDE and the incremental evaluator, it gets notified about AST changes by the IDE. It also allows the evaluator to traverse the input AST during initialization of the computation graph. Later, when the navigator receives an AST change notification, it notifies the incremental evaluator about *relevant* AST changes, as determined by our compiler optimization (Section 4). Since the navigator knows the IDE-internal AST representation, it constitutes a language-independent but IDE-specific component.

The incremental evaluator is responsible for the incremental maintenance of the analysis results. The component is independent of the IDE and of the analyzed language. The evaluator uses the navigator to navigate in the AST and to receive notifications about AST changes; this way, the evaluator does not depend on the internals of the IDE.

The incrementally maintained results of a program analysis can be used to inform the user about new errors or as part of a refactoring in the IDE. This connection closes the loop between the user and our system and shows how incremental analysis supports interactive development.

### 5.2 Implementation for MPS

We instantiated the architecture for the Meta Programming System (MPS) using IncQuery [35] as the incremental evaluator. Both tools are available as open-source software, as is our implementation.<sup>2</sup>

MPS is an IDE that uses *projectional editing* [38] instead of a parser-based approach. When editing the program in a projectional editor, every user edit (for example, inserting an operator) directly corresponds to an AST change. After an AST change, a projectional editor renders new projection from the changed AST based on projection rules of the AST nodes and displays it to the programmer. Projectional editing is well-suited for incremental program analysis because the user’s edits directly correspond to incremental AST changes and no incremental parsing is necessary.

Our system reuses the incremental graph pattern matching component of IncQuery. This component realizes the computation graph presented in Section 2. The component expects the graph patterns to be specified using the Java API PSystem.<sup>3</sup> We implemented the IncA compiler to translate graph patterns into a PSystem specification. After startup, IncQuery uses the navigator to initialize its computation graph and to retrieve AST changes.

### 5.3 Applicability in other IDEs

The applicability of our solution is ultimately determined by the granularity of an IDE’s incremental change notifications as the analyzed program changes. We see three kinds of IDEs where our solution can be applied efficiently. (1) Projectional IDEs (like MPS) where code manipulations directly correspond to incremental AST change events. (2) Graphical IDEs, which, like projectional IDEs, also directly manipulate structures and can thus easily derive AST changes after a change. (3) Finally, textual IDEs that are backed by an incremental parser (cf. [27] for example incremental parsers and the Eco language workbench [8] which relies on an incremental parser). In this case, the degree of incrementality that our approach can achieve depends on the granularity of the incremental AST differences the parser can provide.

## 6. CASE STUDIES

To validate our approach, we have used IncA to implement three program analyses for mbeddr C and one program analysis for Java. mbeddr C is an extensible C dialect and IDE for embedded software built on top of MPS. This section describes the program analyses and gives details about their implementation, while Section 7 contains the performance evaluation. We show only the most interesting parts of the implementations; the full implementation is available online.

### 6.1 Control Flow Analysis

The introductory example in Section 2 already gave an intuition about control flow analysis. The incremental construction of a control flow graph (CFG) is an important building block for incremental, flow-sensitive analyses such as the flow-sensitive points-to analysis described next. These two analyses combined enable further precise analyses such as uninitialized read and unused assignment analysis.

We implemented an incremental control flow analysis that handles all of mbeddr C, including conditionals (`if`

<sup>2</sup><https://szabta89.github.io/projects/inca.html>

<sup>3</sup><https://wiki.eclipse.org/EMFIncQuery/DeveloperDocumentation/PSystem>

$$\frac{}{u \rightarrow v} \quad \frac{}{u = \&v} \quad \frac{}{u = x} \quad \frac{}{u = *x} \quad \frac{}{x = y}$$

Figure 8: Andersen’s rules for points-to analysis

and **switch**), loops (**for**, **while**, and **do while**), and jumps (**break** and **continue**). The implementation follows the style of the introductory example, extending the `cFlow` relation with further alternatives to handle all control statements. The complete control flow analysis produces a CFG where the nodes not only represent statements of the program, but also other control flow points like the alternative **case** branches of a **switch** statement or the **else if** parts of an **if** statement. To this end, we defined an interface `ICFGNode` as supertype for all nodes that can appear in the CFG. We use this type in the points-to analysis as well.

## 6.2 Points-to Analysis

Our second case study is a points-to analysis for mbeddr C. Given a variable that stores a pointer, the goal of a points-to analysis is to identify the possible targets of the variable. There is a vast amount of research in this area [22, 32, 41], because the precision of points-to analysis directly benefits optimizations such as lock elision in a concurrent system and program analyses such as uninitialized read analysis.

We represent the result of a points-to analysis as a relation `PointsTo(from,to)`, which consists of those tuples where both **from** and **to** are variables and **from** potentially points to **to** at runtime. A points-to analysis is sound if `PointsTo` is a conservative approximation of the actual targets of the variables. A well-known algorithm for computing the `PointsTo` relation is Andersen’s algorithm [1]. It considers four basic kinds of assignments as shown in Figure 8 and derives the points-to relation for the whole program from them.

Our points-to analysis in IncA builds on Andersen’s rules but extends them in three ways. First, by implementing the analysis in IncA we immediately improve the run time after code changes through incrementality. Second, we add flow-sensitivity by building on top of our incremental control flow analysis. Third, we do not require the code to only use the four kinds of assignments in Andersen’s rules, rather support all of mbeddr C except pointer arithmetics.

Figure 9 shows an excerpt of the points-to analysis in IncA. We use three main pattern functions for flow-sensitive points-to analysis. Function `pointsToBefore(n,u)` computes the potential targets of variable **u** before the execution of node **n** of the CFG. We compute `pointsToBefore` by promoting the bindings of `pointsToAfter` along the control flow graph. Function `pointsToAt(n)` computes the effect of node **n** in the form of changed bindings **(x,y)**. We describe function `pointsToAt` in greater detail below. Function `pointsToAfter(n,u)` yields the potential targets of variable **u** after the execution of node **n**. If there is no new binding at all at node **n** (first alternative) or the new binding has no effect on variable **u** (second alternative), `pointsToAfter` retains the targets described by `pointsToBefore`. Otherwise, if node **n** affects variable **u** (third alternative), `pointsToAfter` yields the new targets of **u**.

A single CFG node can contain multiple assignments due to expression nesting as in `(x=&u)+(y=&v)`. Function `pointsToAt(n)` gathers all assignments that occur in node **n** and computes the corresponding points-to tuples. Given an assignment, we can extract the pointer variable **u** from the assignment’s left hand side expression and the pointed-to variable **v** from the right with Andersen’s rules. However,

```

def pointsToBefore(n:ICFGNode, u:Var) : Var = {
  pred := cFlow(n)
  return pointsToAfter(pred, u)
}
def pointsToAfter(n:ICFGNode, u:Var) : Var = {
  assert undef pointsToAt(n)
  return pointsToBefore(n, u)
} alt {
  (x, y) := pointsToAt(n)
  assert x != u
  return pointsToBefore(n, u)
} alt {
  (x, y) := pointsToAt(n)
  assert x == u
  return y
}
def pointsToAt(n : ICFGNode) : (Var, Var) = {
  (lhs, rhs) := assignmentAt(n)
  u := varInExprLeft(lhs)
  v := varInExprRight(rhs)
  return (u, v)
}

```

Figure 9: Flow-sensitive points-to analysis in IncA

```

def varInExprLeft(lhs : Expression) : Var = {
  return varInExpr(lhs)
} alt {
  assert lhs instanceof DerefExpr
  e := lhs.expression
  u := varInExprLeft(e)
  n := ancestorCFGNode(lhs)
  v := pointsToBefore(n, u)
  return v
}
def varInExprRight(rhs : Expression) : Var = {
  assert rhs instanceof AddressOfExpr
  e := rhs.expression
  return varInExpr(e)
} alt {
  u := varInExpr(rhs)
  n := ancestorCFGNode(rhs)
  v := pointsToBefore(n, u)
  return v
} alt {
  assert rhs instanceof DerefExpr
  e := rhs.expression
  u := varInExprRight(e)
  n := ancestorCFGNode(rhs)
  v := pointsToBefore(n, u)
  return v
}

```

Figure 10: Extracting points-to variables in IncA

depending on the side of the assignment, the computation is different. `pointsToAt` calls `varInExprLeft` to look up the pointer variable and `varInExprRight` to obtain the pointed-to variable and returns them as a new points-to tuple.

Function `varInExprLeft` in Figure 10 computes the pointer variable of an assignment’s left-hand side expression. If the expression is a plain variable reference, function `varInExpr` from Figure 6 extracts the variable. Otherwise, based on the fourth Andersen rule (Figure 8), a pointer dereferencing `*e` requires the lookup of the points-to targets of **e**. Function `varInExprLeft` calls itself recursively on **e** to handle multiple dereferencings `**e`, until finally reaching a plain variable. We look up the points-to targets of the dereferenced variable **u** by calling `pointsToBefore`, using `ancestorCFGNode` to retrieve the surrounding CFG node. Function `varInExprRight` implements three alternatives corresponding to the first three Andersen rules (in order). The implementation for deref-

```

def CI_CONFUSED_INHERITANCE(c: Class): Void = {
  1  assert c.isFinal == true
  2  member := c.member
  3
  4  assert member instanceof Field
  5  assert member.visibility instanceof Protected
  6
}

```

Figure 11: FindBugs CI\_CONFUSED\_INHERITANCE in IncA erencing (second alternative) is identical to `varInExprLeft` and the other two alternatives are straightforward.

### 6.3 Well-formedness Checks for mbeddr C

We implemented four well-formedness checks for mbeddr C and its language extensions. While the control flow analysis and the points-to analysis inspected each function declarations in separation, our well-formedness checks require global knowledge about the source code. This case study shows that IncA scales to support whole-program analyses. The checks are as follows:

**CYCLE** mbeddr C provides modules for organizing code. This check detects cyclic module dependencies.

**GLOBAL** This check detects conflicting global variables with the same name across modules.

**REC** This check detects recursive functions by construction and inspection of a call graph. In embedded systems with constrained memory, the stack space required for recursive functions is often unacceptable.

**COMP** mbeddr C supports interfaces and composable components. This check detects components that fail to implement all functions declared by their interfaces.

### 6.4 FindBugs for Java

FindBugs [20] is a suite of predefined patterns to detect potential bugs in Java code. To show that our system is independent of the analyzed language, we implemented 10 FindBugs analyses in IncA for MPS’ Java dialect. Apart from a few language extensions, this Java language is identical to the original Java language. As an example, we show the implementation of the CI\_CONFUSED\_INHERITANCE rule in Figure 11. It detects `final` classes that have at least one protected field. Since the class is final, it cannot be subclassed and the field should be private or public. The implementation runs incrementally thanks to IncA.

## 7. PERFORMANCE EVALUATION

In this section, we present the performance evaluation of our system for the case studies introduced in Section 6. We answer the following questions:

**(Q1) Run time:** How does the run time of IncA program analyses compare to their non-incremental counterpart?

**(Q2) Memory:** How does the memory requirement of IncA analyses compare to their non-incremental counterpart?

**(Q3) Optimization impact:** How does our optimization (Section 4) affect the run time and memory requirements?

### 7.1 Evaluation Setup

For each case study, we start with an initial code base (introduced below). After the initial, non-incremental run of the analysis, we programmatically make 100 updates of the code base and run the analysis after each update. Each update consists of 1 to 20 random code changes, such as duplicating a statement, deleting a function, renaming a variable, or in-

roducing a new import. This approach allows us to imitate how a user would modify the source code.

We measure the wall-clock time of processing the initial code and of processing each update step. For the memory measurement, we call the garbage collector after each analysis run and measure the required heap memory. We subtract the heap memory used before running the first analysis to obtain the memory usage of our system. We repeat each measurement five times and discard the results of the first and second run to account for Java VM warm-up.

As the first benchmark, we run the control flow and points-to analysis on the Toyota ITC code<sup>4</sup>, a collection of C code snippets with intentional bugs to test the precision of static analysis tools. The code base comprises about 15,000 lines of C code. We compare the performance of the incremental analyses to a non-incremental flow-sensitive points-to analyses that was already available in mbeddr. The two analyses produce exactly the same results on the benchmark.

The second benchmark was running the well-formedness checks on a commercial Smart Meter software implemented in mbeddr C [36]. A smart meter is an electric meter that continuously records the consumption of electrical power, calculates derived quantities, and sends the data back to the utility provider for monitoring and billing. The whole project comprises about 44,000 lines of mbeddr C code. For comparison, we implemented non-incremental well-formedness checks in MPS Java that produce exactly the same results.

Running the 10 FindBugs checks constituted the third benchmark; we ran it on the Java implementation of the mbeddr importer which is responsible for migrating legacy C code to mbeddr C. The importer comprises about 10,000 lines of MPS Java code. Because of the MPS Java code base, we would need to generate textualized Java code after every code change to be able to use the original FindBugs tool. For our large code base this is impractical, and thus for this benchmark we do not have a non-incremental counterpart.

We ran the benchmarks on a 64-bit OSX 10.10.3 machine with an Intel Core i7 2.5 GHz processor and 16 GB of RAM, using MPS version 3.3 and Java 1.8.0\_65. The raw data and the sequence of code changes are available online.<sup>5</sup>

### 7.2 Run Time Measurements (Q1 & Q3)

Figure 12 (A) - (C) show the results of our run time measurements. For points-to analysis and well-formedness checks we show the results of the incremental and non-incremental solutions using a logarithmic scale, for FindBugs we only show the incremental results on a linear scale.

Box plots (A) and (B) immediately show that incremental program analyses in IncA perform significantly better than non-incremental analyses. Box plots (A) - (C) also show that the optimization has a significant impact on the run time. The following table summarizes the median run time data:

	Non-inc.	Inc w/o opt		Inc w/ opt	
		init	update	init	update
Points-to (A)	5.8s	5.6s	83.2ms	1.6s	23.3ms
W.-form. (B)	209ms	12.1s	104.8ms	1.2s	12.8ms
FindBugs (C)	n/a	4.5s	40.2ms	2.3s	7ms

The initialization times, especially for the optimized versions, do not pose an unduly run time requirement considering that

<sup>4</sup><https://github.com/regehr/itc-benchmarks>

<sup>5</sup><https://szabta89.github.io/projects/inca.html>



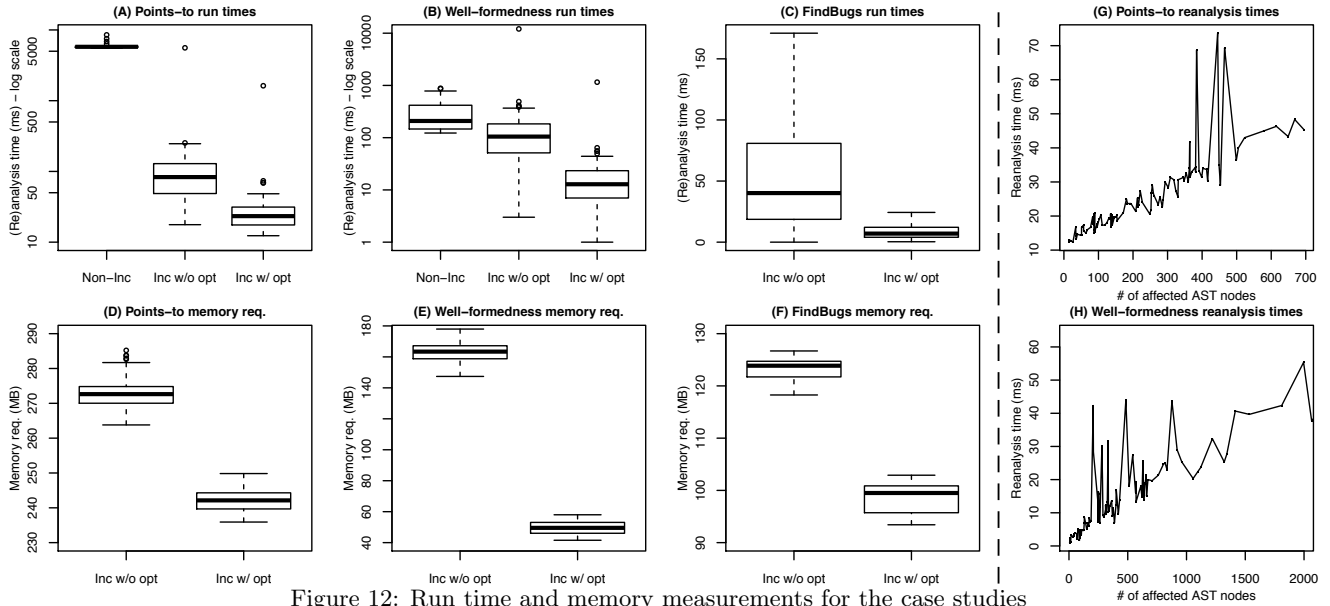


Figure 12: Run time and memory measurements for the case studies

loading large programs into MPS also takes a few seconds. After initialization, incremental analysis without optimization achieves speedups of  $A=70x$  and  $B=2x$  compared to non-incremental analysis. With optimization, we even achieve speedups of  $A=249x$  and  $B=16x$ . Indeed, the optimization accounts for an additional speedup of 2 - 10x for initialization and of 3 - 8x for change-processing time. IncA and its optimization provide good runtime performance across analyses and for both analyzed languages.

Figure 12 (G) and (H) show the points-to and well-formedness analysis times as a function of the input change size (the number of deleted or added AST nodes). IncA scales well, because the plots remain roughly linear with increasing input change sizes. We conclude:

**(Q1)** Incremental program analysis with IncA provides significant speedups of up to 249x compared to non-incremental analysis. The analyses scale linearly with increasing input change sizes.

**(Q3)** The IncA compiler optimization improves initialization time by 2–10x and change-processing time by 3–8x.

### 7.3 Memory Measurements (Q2 & Q3)

Figure 12 (D) - (F) show the results of our memory measurements. We measured the memory required by incrementality for each of the 100 update steps and created the box plots from this data. We summarize the median memory requirements in the following table:

	Inc w/o opt	Inc w/ opt
Points-to (D)	273MB	242MB
Well-form. (E)	163MB	50MB
FindBugs (F)	124MB	100MB

The points-to analysis is the most complex case study and it has the biggest memory requirement, because its computation graph caches major part of the input AST to compute a complete CFG and to handle a large variety of assignments, including nested ones. The optimization helps to reduce the memory requirement with  $A=11\%$ ,  $B=69\%$ , and  $C=19\%$ . Based on our experience with real world usage of MPS, the IDE typically requires about 1.2GB of memory. This means

that the optimized analyses have a memory overhead of  $A=20\%$ ,  $B=4\%$ , and  $C=8\%$  relative to MPS. We conclude:

**(Q2)** For real world scenarios, incremental program analysis with IncA requires an acceptable amount of 20% additional memory for our most complex case study.

**(Q3)** The IncA compiler optimization reduces the required memory by 11 - 69%.

## 8. RELATED WORK

Improving the performance of program analyses has attracted a lot of research, because of their widespread use in compilers and IDEs. In particular, the use of incrementality to speed up program analyses has a long tradition.

**Specialized algorithms** While we present a *framework* for incremental program analyses, the manual enhancement of *specific* program analyses to support incrementality is also subject to research. Yur et al. propose an algorithm for incremental flow-sensitive and context-sensitive points-to analysis [41]. The paper assumes that the CFG is maintained incrementally. This in itself is a challenging problem which we address as part of our work. Saha and Ramakrishnan propose an incremental flow-insensitive points-to analysis for a subset of C using logic programming [32]. Lu et al. present a context-sensitive and field-sensitive points-to analysis for Java based on context-free language reachability [22].

All of these approaches target only a particular analysis (e.g. points-to analysis). There are also many algorithms [7, 25, 18, 26] tailored specifically to incrementally handle a *class* of analyses (e.g. data-flow analyses). IncA goes one step further than those because it supports a wider spectrum of analyses (data-flow, syntactic analyses such as well-formedness, FindBugs) as a language-agnostic framework.

The previously mentioned algorithms come with their own incrementalization engine that could play the role of the incremental evaluator in our architecture (although limiting expressivity to a particular class of analyses). Once embedded into our architecture, their efficiency could be improved with our optimizations.

**Analysis Frameworks** Several systems in model-driven development incrementally reapply consistency rules on mod-

els [12, 16, 6]. These systems are only incremental in the sense that they *selectively* reapply affected consistency rules after a change; once selected, the rules run *non-incrementally* on the *whole* input. This form of incrementalization is not practical for complex program analyses or for large inputs. In contrast, our system incrementally reanalyzes only those program entities that are actually affected by a change.

EMF-IncQuery is a framework for incremental queries over EMF models [35]. Like IncA, it is based on the IncQuery evaluator. We have generalized EMF-IncQuery in several ways. First, we have designed the IncA DSL for the description of program analyses that abstracts from graph patterns and supports more conventional descriptions as forward or backward analyses. Second, we extended our runtime system with the data-flow analysis-based optimization, whereas EMF-IncQuery requires manual registration of relevant parts of the metamodel. Third, we describe an architecture that supports the integration with IDEs other than Eclipse. Finally, the focus of EMF-IncQuery is efficient model queries, while we show that graph patterns can be used to implement program analyses that scale to real world programs.

Wachsmuth et al. introduce a language-independent task engine for incremental name and type analysis in the Spoofox language workbench [39]. Evaluation starts by collecting analysis tasks, which are executed in a second step. When the analyzed program changes, the task engine recollects analysis tasks and executes only the affected ones. IncA, in contrast, is not limited to name or type analysis, but supports a wider range of analyses.

Arzt and Bodden present an extension of the IFDS framework [31] that selectively re-derives changed parts of a program’s data-flow graph upon program manipulation, enabling incremental data-flow analysis. The IFDS framework reduces data-flow problems into a graph reachability problem and associates semantic information to program nodes whereas IncA relates program nodes. There are analyses (e.g., points-to) that can be cast as both, but there are certain analyses that are not supported by either one of the frameworks. For example, IncA cannot handle analyses that require the generation of runtime data (e.g., an interval for an interval analysis), while IFDS cannot handle the more syntactic analyses (e.g., well-formedness or FindBugs). Supporting semantic analyses in IncA would require IncA extensions to generate and incrementally maintain runtime data that is not in the AST. However, this is non-trivial because such an extension must consider the trade-off between the expressive power needed for computing derived data and the feasibility of incrementalizing these computations. We investigate this direction in future work.

**Datalog-based analyses** The IncA runtime system translates analysis code into graph patterns, and we showed that the solution can scale to real-world programs. Datalog, a logic programming language used in deductive databases, is often used as a definition language of program analyses [40, 5, 29]. In fact, IncQuery’s graph patterns and Datalog with stratified negation [15] and recursion share the same expressive power [21, p. 225]. This suggests that Datalog could replace IncQuery as the target language for IncA and a corresponding incremental Datalog backend could be used as the evaluator in our system. However, to the best of our knowledge there is only the commercial LogicBlox [2] backend which employs incrementalization. From the literature, it is clear that LogicBlox scales to large inputs for from-scratch

analyses, but the performance of the incremental part is not documented. While there are specialized algorithms for incrementalizing Datalog queries [10, 11], we emphasise that our contributions build around an off-the-shelf evaluator. Also, abstracting from the relational nature of Datalog with IncA helps language developers to deal with abstractions that they are usually already familiar with, such as functions, direction from input to output, and assignments. This is important, because, for DSLs it is often infeasible to spend large efforts on program analyses; a straightforward implementation approach is crucial.

The DOOP framework [33] uses Datalog and LogicBlox to define various (flow-insensitive) points-to analyses for Java. The authors of DOOP pose the question whether a high-level language can be expressive enough to implement various program analyses. They show that Datalog is well-suited for Java points-to analyses. However, our work shows that a high-level language (which can itself be analyzed) is also useful for other kinds of syntactic analyses in a language-agnostic way. We will experiment with extensions of IncA for generating runtime data, which would further extend the kinds of analyses that IncA could support.

**General-purpose incrementalization** i3QL [23] makes incremental computations available as an embedded Scala DSL using an SQL-like syntax. Its runtime system builds on relational algebra and applies optimizations known from database engines, but, unlike IncA, it does not detect and filter irrelevant program changes. Adapton [17] is an ML-style general-purpose language for incremental self-adjusting computations. The runtime system of Adapton tracks memory dependencies in order to retrigger computations when a change to a data structure occurs. Compared to our evaluation, these tools were tested only on small programs with simple analyses, and it is thus unclear whether they can scale to support complex program analysis of large code bases.

**Analysis DSLs** Other researchers have proposed DSLs for specific program analyses, but, in contrast to IncA, do not provide incrementalization. Examples include the declarative DSL for CFG construction in the JastAdd compiler [34, 13], the data-flow rules of DCFlow [19], fact extraction with De-Facto [4], and MPS’ DSL for constructing data-flow graphs.

## 9. CONCLUSIONS

We presented IncA, a DSL for the definition and efficient evaluation of incremental program analyses in IDEs. IncA provides a declarative notation for analyses in the style of pattern functions that our optimizing compiler translates into graph patterns. We demonstrated the applicability of IncA by developing incremental program analyses for C and Java. Our performance evaluation shows that IncA provides significant speedups compared to non-incremental analyses and acceptable memory overhead.

We plan to use IncA in the future to enforce secure coding standards (e.g. Misra, CERT) in mbeddr. These standards define well-formedness rules which IncA can check incrementally as the program is edited.

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